2024 IEEE International Symposium on High-Performance Computer Architecture (HPCA 2024)

Edinburgh, United Kingdom
2-6 March 2024

Pages 1-612

IEEE Catalog Number: CFP24013-POD
ISBN: 979-8-3503-9314-9

****** This is a print representation of what appears in the IEEE Digital Library. Some format issues inherent in the e-media version may also appear in this print version.***

IEEE Catalog Number:	CFP24013-POD
ISBN (Print-On-Demand):	979-8-3503-9314-9
ISBN (Online):	979-8-3503-9313-2
ISSN:	1530-0897

Additional Copies of This Publication Are Available From:

Curran Associates, Inc
57 Morehouse Lane
Red Hook, NY 12571 USA
Phone: (845) 758-0400
Fax: (845) 758-2633
E-mail: curran@proceedings.com
Web: www.proceedings.com

2024 IEEE International Symposium on High-Performance Computer Architecture (HPCA)

HPCA 2024

Table of Contents

Security: Side-Channel, Session 1/A

Reconfigurable Architecture & FPGA, 1/B

GNN, Session 1/C

Accelerators & Memory Systems, Session 2/A

Security, Session 2/B

Near Memory Processing, Session 2/C

Computational SSD, Session 3/A

Side-Channel & Microarchitecture, Session 3/B

Accelerator (non-DNN), Session 3/C

Microarchitecture, Session 5/A

Rowhammer, Session 5/B

Best of CAL, Session 5/C

SSD, Session 6/A

Emerging Technology, Session 6/B

Accelerator, Session 6/C

Distributed DNN & Training, 7/A

IoT/Edge, Session 7/B

Datacenter & Networks, Session 7/C

Industrial Track, Session 9

Accelerator, Session 10/A

GPU, Session 10/B

Cache & Memory System, Session 10/C