

Semiconductor Technology for Ultra Large Scale Integrated Circuits and Thin Film Transistors (ULSIC vs TFT 8)

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Monday, May 15, 2023

- 06:30 – 08:00 **Breakfast** (Terrace Brasserie Restaurant)
- 08:00 – 08:05 **Open remark**
Yue Kuo, Texas A&M University
- Session: General Topics**
Chair: Yue Kuo, Texas A&M University
- 08:05 – 08:30 **Invited**
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Shunpei Yamazaki, Semiconductor Energy Laboratory Co., Ltd
- 08:30 – 08:55 **Invited**
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 Laurie E. Calvet, LPICM, CNRS-Ecole Polytechnique
 Shinji Migita, AIST
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Session: P-channel Oxide Materials for TFTs

Chair: Olivier Bonnaud, University of Rennes

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- 06:30 – 08:00 **Breakfast** (Terrace Brasserie Restaurant)
- Session: Devices and Circuits I**
Chairs: Hitoshi Kunitake, Semiconductor Energy Laboratory Co., Ltd
I-Chun Cheng, National Taiwan University
Radu A. Sporea, University of Surrey
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- 20:00 – 21:30 **Poster session (Remove posters at 21:30)**
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