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116	23VLSI002	Performance Analysis of FIR Filters using High Speed VLSI Adders for FECG monitoring <b>Preethi D, Sasikala G, Gnanaprakash V, Sathyasri M, Archana M</b>	661-666
117	23VLSI003	Area and Power Efficient Transposition Buffer design for 2D-DCT Architecture in Video Applications <b>Preethi D, Sasikala G, Gnanaprakash V,Sathyasri M, Saranya</b>	667-671
118	23VLSI004	Analysis of RC Delay Using Various Multi Level Interconnects <b>Dr.S. Aathilakshmi, G. Mugundan, Sanjay Kumar K, D. Mohamed Vaheen, Harinesh Vijay Blessy Sam.A. S</b>	672-677
119	23VLSI005	FPGA Implementation of Energy Efficient Vedic Multiplier Using CSA Architecture <b>Dr.S. Aathilakshmi, G. Mugundan, Sanjay Kumar K, D. Mohamed Vaheen, Harinesh Vijay, V.N. Dhenishaa</b>	678-685
120	23VLSI008	Comparison of RF Performance of Novel SSE Shaped Microstrip Patch Antenna Using Arlon AD300A and Tcon RF30 Materials for S-Band Applications <b>Prakash L, Anitha G</b>	686-690
121	23VLSI015	Power and Area Optimization Techniques for Reconfigurable Inverse Discrete Cosine Transform FPGA for High Performance Computation Electromagnetics <b>Dr.R.Radhika, Dinesh.S, Harini.S, Kaviya.S, Mallapadi Mohammed Hamed, Priyadarshini.M</b>	691-695