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TD3-1	Fabrication and Evaluation of Microfluidic Organic-Light Emitting Diode Having a Fluorine-Doped Tin Oxide Cathode	Ryuto Ikeda <sup>1</sup> , Jun Mizuno <sup>2</sup> , Takashi Kasahara <sup>1</sup> , <sup>1</sup> Hosei University / Japan, <sup>2</sup> National Cheng Kung University / Taiwan	117
TD3-2	Development of Ultra-Low Chlorine Epoxy Resins for Highly Reliable Electronics	Ryo Yoshimura, Wataru Urano, Yuuki Asuma, Kazumasa Ota, Mitsubishi Chemical / Japan	119
TD3-3	Development of Laminate Materials with Low D <sub>f</sub> Using Novel Functionalized PPE	H. Yamamoto <sup>1</sup> , K. Iwase <sup>1</sup> , H. Fukuoka <sup>1</sup> , S. Otani <sup>1</sup> , M. Harada <sup>2</sup> , <sup>1</sup> Asahi Kasei, <sup>2</sup> Kansai University / Japan	121

<b>TD3-4</b>	Liquid Crystal Mesogenic Epoxy Modified Cyanate Ester Resin	S. Yanaura, M. Harada, Kansai University / Japan	<b>123</b>
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#### TD4: Metal Pastes

<b>TD4-1</b>	Reliability Evaluation of SiC/Cu Substrate Die-attached Modules with Sintered Cu Joint and Pb-free Solder	Ming-chun Hsieh <sup>1</sup> , Aiji Suetake <sup>1</sup> , Zheng Zhang <sup>1</sup> , Rieko Okumura <sup>1</sup> , Kei Anai <sup>2</sup> , Satoshi Konno <sup>2</sup> , Katsuaki Suganuma <sup>1</sup> , <sup>1</sup> Osaka University, <sup>2</sup> Mitsui Mining & Smelting (MITSUI KINZOKU) / Japan	<b>125</b>
<b>TD4-2</b>	Effect of Chemical Factors on Electrical Conductivity of Interconnection Between Carbon-Nanotube-Filled Conductive Pastes and Copper Electrodes	Masahiro Inoue, Subaru Tsujimura, Gunma University / Japan	<b>127</b>

#### TE1: LED

10:10-11:50 Thursday, April 20

<b>TE1-1</b> <Session Invited>	Properties of Eu(III)- $\beta$ -Diketonates with Different Phosphine Oxide-Structures and Their Future Applications for LEDs	Hiroki Iwanaga, Toshiba / Japan	<b>129</b>
<b>TE1-2</b> <Session Invited>	Monolithic Vertically Stacked RGB LEDs for Small Micro-LED Displays with Ultrahigh Definition	Yasufumi Fujiwara, Shuhei Ichikawa, Dolf Timmerman, Jun Tatebayashi, Osaka University / Japan	<b>131</b>
<b>TE1-3</b> <Session Invited>	Optimum Specification of LED for Strawberry Cultivation in a Closed Space Like a Building	Hirohisa Sato, Akinobu Habe, Kyowa / Japan	<b>132</b>
<b>TE1-4</b> <Session Invited>	405nm LED-based White LED Technologies to Open the New ERA: General Illumination and Inactivation of Virus	Atsushi Okuno <sup>1</sup> , Jang Uk An <sup>2</sup> , <sup>1</sup> Green Planets / Japan, <sup>2</sup> ALLIX / Republic of Korea	-

#### TE2: IMPACT Session

12:40-14:20 Thursday, April 20

<b>TE2-1</b> <Session Invited>	Simulation of Laser-Assisted Bonding Process by the Phase-field Method	Tai-Yu Pan, Wen-Dung Hsu, Nation Cheng Kung University / Taiwan	-
<b>TE2-2</b> <Session Invited>	Understanding Peculiar Behaviors of Electronic Materials Through Phase Diagrams	Sinn-wen Chen, National Tsing Hua University / Taiwan	-
<b>TE2-3</b> <Session Invited>	Predicting Void Reduction of Flip Chip Package in the Pressure Oven	L.H. Shen <sup>1</sup> , C.T. Wu <sup>1</sup> , D.C. Hu <sup>2</sup> , E.H. Chen <sup>2</sup> , Jeffrey C.B. Lee <sup>3</sup> , <sup>1</sup> CoreTech System (Moldex3D), <sup>2</sup> SiPlus, <sup>3</sup> iST-Integrated Service Technology / Taiwan	-
<b>TE2-4</b> <Session Invited>	Advanced Substrate for Multi-chip Integration	Yu-Hua Chen, Unimicron / Taiwan	-

#### TE3: ISMP Session-1

14:30-16:10 Thursday, April 20

<b>TE3-1</b> <Session Invited>	Electrodeposition of Invar alloy for FMM Applications	Jae Ho Lee, Hongik University / Republic of Korea	-
<b>TE3-2</b> <Session Invited>	Nanoscale Copper Roughness Formation and Its Application	Bomook Chung, YMT / Republic of Korea	-
<b>TE3-3</b> <Session Invited>	Effective Thermal Property Mapping of Semiconductor Packages for Thermal Management Based on Convolution Neural Network	Jeong-Hyeon Park <sup>1</sup> , Kyung-bin Kim <sup>1</sup> , Hwanjoo Park <sup>3</sup> , Sunggu Kang <sup>3</sup> , Sungho Mun <sup>3</sup> , Jaechoon Kim <sup>3</sup> , Eun-Ho Lee <sup>1,2</sup> , <sup>1</sup> Sungkyunkwan University, <sup>2</sup> Sungkyunkwan University, <sup>3</sup> Samsung Electronics / Republic of Korea	-
<b>TE3-4</b> <Session Invited>	Strategies for Mechanically Reliable Thin-Film Flexible Electronics	Taek-Soo Kim, KAIST / Republic of Korea	-

#### TE4: ISMP Session-2

16:50-18:30 Thursday, April 20

<b>TE4-1</b> <Session Invited>	Microstructure and Mechanical Property of Soldering using Photonic Energy	Seung-Boo Jung, Taejoon Noh, Kyung Deuk Min, Sungkyunkwan University / Republic of Korea	-
<b>TE4-2</b> <Session Invited>	A Novel Selective EMI Shielding Process by Exploiting Tape Attach and Detach Sub-processes	Wonyong Choi <sup>1</sup> , David Bokwoo Han <sup>1</sup> , Keejun Han <sup>2</sup> , <sup>1</sup> Genesem, <sup>2</sup> Hansung University / Republic of Korea	-
<b>TE4-3</b> <Session Invited>	Inkjet Printing Technology for Semiconductor Packaging	Seog Soon Kim, UniJet / Republic of Korea	-



<b>TE4-4</b> <Session Invited>	Advanced Metallizations for Next Generation Semiconductor Packaging Technology	Bongyoung Yoo, Hanyang University / Republic of Korea	<b>134</b>
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### FA1: Pan-pacific Session

9:00-10:40 Friday, April 21

<b>FA1-1</b> <Session Invited>	The New Tech Frontier for Healthcare	Matthew Hudes, bdlBiologx / USA	-
<b>FA1-2</b> <Session Invited>	Breakthrough Technology to Improve Semiconductor X-Ray Results	Keith Bryant, Excillum / Sweden	-

### FA2: Chiplet-1

10:50-12:30 Friday, April 21

<b>FA2-1</b> <Session Invited> 50min.	Challenges and Opportunities with Chiplets? Where Do We Go Next?	Jan Vardaman, TechSearch International / USA	-
<b>FA2-2</b> <Session Invited> 50min.	Advanced Packaging Technology Solutions and Hybrid Design Platform for Chiplets Integration	Lihong Cao, ASE (US) / USA	-

### FA3: Chiplet-2

13:20-15:00 Friday, April 21

<b>FA3-1</b> <Session Invited> 50mn.	Evolution of BEOL Technology with Scaling	Kazuyoshi Ueno, Shibaura Institute of Technology / Japan	-
<b>FA3-2</b> <Session Invited>	Overview and Progress of the Chiplet Integration Platform Consortium	Meiten Koh <sup>1</sup> , Yoichiro Kurita <sup>2</sup> , Yasuhiro Morikawa <sup>3</sup> , Ichiro Kono <sup>4</sup> , Takafumi Fukushima <sup>5</sup> , Katsuaki Suganuma <sup>6</sup> , <sup>1</sup> Taiyo Ink MFG., <sup>2</sup> Tokyo Institute of Technology, <sup>3</sup> ULVAC, <sup>4</sup> AOI Electronics, <sup>5</sup> Tohoku University, <sup>6</sup> Osaka University / Japan	-
<b>FA3-3</b> <Session Invited>	Wafer-Level Chiplets Integration Platform	Wei-Chung Lo, Industrial Technology Research Institute / Taiwan	-

### FA4: Chiplet-3

15:20-17:00 Friday, April 21

<b>FA4-1</b> <Session Invited>	Chiplet Technologies for HPC and AI	Dale McHerron, IBM / USA	-
<b>FA4-2</b> <Session Invited>	The Emergence of Chiplet Interconnect Technology: Breaking Boundaries Between FEOL and BEOL	Fumihiro Inoue, Yokohama National University / Japan	-
<b>FA4-3</b> <Session Invited> 50min.	3D Super Chip Concept to Build a New Era of Chiplet and Heterogeneous Integration	Takafumi Fukushima, Tohoku University / Japan	-

### FB1: Optoelectronics-1

9:00-10:40 Friday, April 21

<b>FB1-1</b> <Session Invited> 50min.	IOWN Brought by Photonics-Electronics Convergence Devices	Yuzo Ishii, NTT / Japan	-
<b>FB1-2</b> <Session Invited> 50min.	Nanophotonics Toward On-chip Photonic Integration	A. Shinya <sup>1,2</sup> , S. Kita <sup>1,2</sup> , K. Ikeda <sup>1,2</sup> , K. Nozaki <sup>1,2</sup> , T. Ishihara <sup>4</sup> , S. Matsuo <sup>1,3</sup> , M. Notomi <sup>1,2</sup> , <sup>1</sup> NTT Nanophotonics Center, <sup>2</sup> NTT Basic Research Laboratories, <sup>3</sup> NTT Device Technology Laboratories, <sup>4</sup> Nagoya University / Japan	-

### FB2: Optoelectronics-2

10:50-12:30 Friday, April 21

<b>FB2-1</b> <Session Invited> 50min.	Current Status and Future Prospects of Optical Interconnection Based on Si Photonics Technology	Kazuhiko Kurata, AIO Core / Japan	-
<b>FB2-2</b>	Quantum Dot Color Conversion Film with Enhanced Color Rendering Performance	Yuanjie Cheng, Jeffery C. C. Lo, Xing Qiu, Hua Xu, Mian Tao, S. W. Ricky Lee, Hong Kong University of Science & Technology / Hong Kong	<b>135</b>

<b>FB2-3</b>	A Novel 3D Structure with 2D Addressable VCSEL Arrays and Laser Diode Driver for Solid-State LiDAR	Hirohisa Yasukawa <sup>1</sup> , Kiyohisa Sakai <sup>1</sup> , Masashi Nakazawa <sup>1</sup> , Gyongsok Song <sup>1</sup> , Hideki Watanabe <sup>1</sup> , Yasutaka Higa <sup>1</sup> , Rintaro Koda <sup>1</sup> , Yoshio Konishi <sup>2</sup> , Hayato Kamizuru <sup>2</sup> , Hayato Iwamoto <sup>1</sup> , <sup>1</sup> Sony Semiconductor Solutions, <sup>2</sup> Sony Semiconductor Manufacturing / Japan	<b>137</b>
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### FB3: Ag Sintering Interconnect

13:20-15:00 Friday, April 21

<b>FB3-1</b>	Novel Al /AlN Bonding by Micro-Sized Ag Particles Sinter Joining in Low Temperature Low Pressure Air Conditions	Chuantong Chen <sup>1</sup> , Yang Liu <sup>1</sup> , Minoru Ueshima <sup>2</sup> , Katsuaki Suganuma <sup>1</sup> , <sup>1</sup> Osaka University, <sup>2</sup> Daicel / Japan	<b>139</b>
<b>FB3-2</b>	Effect of Aging and Thermal Shock on the Reliability of Silver Sintered Die Attach for SiC Power Devices	Wangyun Li <sup>1</sup> , Chuantong Chen <sup>1</sup> , Yang Liu <sup>1</sup> , Minoru Ueshima <sup>2</sup> , Takeshi Sakamoto <sup>2</sup> , Katsuaki Suganuma <sup>1</sup> , <sup>1</sup> Osaka University, <sup>2</sup> Daicel / Japan	<b>141</b>
<b>FB3-3</b>	A Novel and Cost-Effective Ag/Si Compositated Paste With Highly Stable Microstructure Maintained in Thermal Shock Cycles	Y. Liu <sup>1</sup> , C. Chen <sup>1</sup> , M. Ueshima <sup>2</sup> , T. Sakamoto <sup>2</sup> , T. Naoe <sup>1</sup> , H. Nishikawa <sup>1</sup> , K. Suganuma <sup>1</sup> , <sup>1</sup> Osaka University, <sup>2</sup> Daicel / Japan	<b>143</b>
<b>FB3-4</b>	Microstructure and Property of Ag Sintered Joint Doping with AlN Nanoparticles	Jianhao Wang, Shogo Yodo, Hiroaki Tatsumi, Hiroshi Nishikawa, Osaka University / Japan	<b>145</b>

### FB4: Cu Interconnect

15:20-17:00 Friday, April 21

<b>FB4-1</b> <Session Invited>	TGV Cu Metallization on Glass Technology Trend	Tetsuya Onishi, Grand Joint Technology / Hong Kong	-
<b>FB4-2</b>	Enhanced Reactivity of Electroless Cu Interconnection by Surface Oxidation Pretreatment	Y. C. Lin <sup>1</sup> , C. H. Shen <sup>1</sup> , C. Y. Hung <sup>1</sup> , P. S. Shih <sup>1</sup> , J. H. Huang <sup>1</sup> , C. L. Kao <sup>2</sup> , Y. S. Lin <sup>2</sup> , Y. C. Hung <sup>2</sup> , C. R. Kao <sup>1</sup> , <sup>1</sup> National Taiwan University, <sup>2</sup> Advanced Semiconductor Engineering Group / Taiwan	<b>147</b>
<b>FB4-3</b>	Molecular Dynamics Simulation of Cu-Cu Solid-State Bonding under Various Bonding Parameters	Hiroaki Tatsumi <sup>1</sup> , C.R. Kao <sup>2</sup> , Hiroshi Nishikawa <sup>1</sup> , <sup>1</sup> Osaka University / Japan, <sup>2</sup> National Taiwan University / Taiwan	<b>149</b>
<b>FB4-4</b>	Study of Cu Micro-via by TOF-SIMS and STEM	Masahiko Nishijima <sup>1</sup> , Ming-Chun Hsieh <sup>1</sup> , Zhang Zheng <sup>1</sup> , Aiji Suetake <sup>1</sup> , Hiroshi Yoshida <sup>1</sup> , Rieko Okumura <sup>1</sup> , Chuantong Chen <sup>1</sup> , Hidekazu Homma <sup>2</sup> , Koji Kita <sup>2</sup> , Katsuaki Suganuma <sup>1</sup> , <sup>1</sup> Osaka University, <sup>2</sup> Okuno Chemical Industries / Japan	<b>151</b>

### FC1: High-Speed, beyond 5G and mmWave

9:00-10:40 Friday, April 21

<b>FC1-1</b>	Design of Dual-Band Antenna in Package with Steerable Beam for 5G mmWave Communication Systems	Sheng-Chi Hsieh, Hong-Sheng Huang, Wen-Chun Hsiao, Cheng-Yu Ho, Chen-Chao Wang, Advanced Semiconductor Engineering / Taiwan	<b>153</b>
<b>FC1-2</b>	Effect of Ni Additive on Electroless Cu Quality for High Density Interconnect PCB Substrate	Zheng Zhang <sup>1</sup> , Ming-Chun Hsieh <sup>1</sup> , Masahiko Nishijima <sup>1</sup> , Aiji Suetake <sup>1</sup> , Hiroshi Yoshida <sup>1</sup> , Rieko Okumura <sup>1</sup> , Chuantong Chen <sup>1</sup> , Hidekazu Homma <sup>2</sup> , Koji Kita <sup>2</sup> , Katsuaki Suganuma <sup>1</sup> , <sup>1</sup> Osaka University, <sup>2</sup> Okuno Chemical Industries / Japan	<b>155</b>
<b>FC1-3</b>	Fabrication of High-speed Signal Transmission Rigid Substrate by Silver-seed Copper Plating Technique	Rei Tamura, Norimasa Fukazawa, Wataru Fujikawa, DIC / Japan	<b>157</b>
<b>FC1-4</b>	Electroless Plating on Coiled Algae to Copper Microcoils and Their Terahertz Electromagnetic Wave Absorption	Keita Arikio <sup>1</sup> , Tomokazu Iyoda <sup>1,2</sup> , Koji Kita <sup>1</sup> , Joonhaeng Kang <sup>1</sup> , Yukihiko Tanida <sup>3</sup> , Naoya Kurahashi <sup>3</sup> , Masahiro Koide <sup>4</sup> , Toshichika Ooki <sup>4</sup> , <sup>1</sup> Okuno Chemical Industries, <sup>2</sup> Doshisha University, <sup>3</sup> Kyoto Prefectural Technology Center for Small and Medium Enterprises, <sup>4</sup> Panac / Japan	<b>159</b>

### FC2: Sensing Device

10:50-12:30 Friday, April 21

<b>FC2-1</b>	Strain-Induced Change of Gas Adsorption Properties of Graphene and its Application to a Gas Sensor	Xiangyu Qiao, Meng Yin, Ken Suzuki, Hideo Miura, Tohoku University / Japan	<b>161</b>
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FC2-3	Development of a Compact Module for Deep-body Temperature Measurement	Shoya Fukui, Nobuaki Hashimoto, Suwa University of Science / Japan	165
FC2-4	LPWA Based Module for Forest Fire Detection	Eunsol Jo, Cheong-Ha Jung, Gu-Sung Kim, Kangnam University / Republic of Korea	167

### FC3: Emerging 3D Integration Technologies

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FC3-2	Direct Bonding of Germanium and Diamond Substrates by Hydrophilic Bonding	Yuki Minowa <sup>1,2</sup> , Takashi Matsumae <sup>1</sup> , Masanori Hayase <sup>2</sup> , Yuichi Kurashima <sup>1</sup> , Hideki Takagi <sup>1</sup> , <sup>1</sup> National Institute of Advanced Industrial Science and Technology, <sup>2</sup> Tokyo University of Science / Japan	171
FC3-3	Protection of Activated Au Surface using Self-assembled Monolayer for Room Temperature Bonding	Kai Takeuchi <sup>1</sup> , Junsha Wang <sup>2</sup> , Tadatomo Suga <sup>2</sup> , Beomjoon Kim <sup>3</sup> , Eiji Higurashi <sup>1</sup> , <sup>1</sup> Tohoku University, <sup>2</sup> Meisei University, <sup>3</sup> the University of Tokyo / Japan	173
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FD1-3	Topology Optimization of a Thermal Conduction Block Using the Thermal Bottleneck	Haruki Takei, Siemens / Japan	181

### FD2: Thermal Management-2

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### FD3: Thermal Management-3

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FD3-1	Issues of Using Unsaturated Heating Time for Transient Thermal Measurement	Tomoaki Hara <sup>1</sup> , Shuhei Fukunaga <sup>2</sup> , Tsuyoshi Funaki <sup>2</sup> , <sup>1</sup> Siemens, <sup>2</sup> Osaka University / Japan	191
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**FE1: Solder-1**

9:00-10:40 Friday, April 21

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**FE2: Solder-2**

10:50-12:30 Friday, April 21

<b>FE2-1</b>	Investigation of the Stability of CuIn <sub>2</sub> in Cu-In Phase Diagram	F. L. Chang, Y. H. Lin, C. R. Kao, National Taiwan University / Taiwan	<b>205</b>
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<b>FE2-3</b>	The Influence of Bi Content on Joint Properties Using Sn-Bi-Zn-In Alloy	H. Nakawaki <sup>1</sup> , H. Tatsumi <sup>1</sup> , C. Yang <sup>2</sup> , S. Lin <sup>2</sup> , H. Nishikawa <sup>1</sup> , <sup>1</sup> Osaka University / Japan, <sup>2</sup> National Cheng Kung University / Taiwan	<b>209</b>
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**FE3: Solder-3**

13:20-15:00 Friday, April 21

<b>FE3-1</b>	Hybrid SnBi/SAC Low-Temperature Solder Bump	Albert T. Wu <sup>1</sup> , Jui-Lin Chao <sup>1</sup> , Yu-Yuan Lai <sup>1</sup> , Chang-Meng Wang <sup>2</sup> , <sup>1</sup> National Central University, <sup>2</sup> Shenmao Technology / Taiwan	<b>213</b>
<b>FE3-2</b>	The Effects of Solution Treatment and Room Temperature Ageing on Mechanical Properties of Sn-37wt%Bi and Sn-57wt%Bi	Xiaozhou Ye <sup>1</sup> , Lei Tao <sup>1</sup> , Stuart D. McDonald <sup>1</sup> , Xin Fu Tan <sup>1</sup> , Keith Sweatman <sup>2</sup> , Kazuhiro Nogita <sup>1</sup> , <sup>1</sup> The University of Queensland / Australia, <sup>2</sup> Nihon Superior / Japan	<b>215</b>
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**FE4: Advanced Package Material**

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<b>FE4-2</b>	1/1 μm Line and Space Cu Wiring with Organic Dielectric Through High Electrical Reliability	Yu Shoji, Takuma Nishimura, Hisashi Ogasawara, Keika Hashimoto, Yuki Masuda, Hitoshi Araki, Masao Tomikawa, Toray Industries / Japan	<b>221</b>
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