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# SiGe, Ge, and Related Compounds 4: Materials, Processing, and Devices

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## Table of Contents

Preface iii

### Chapter 1 SiGe, Ge, and Related Compounds Plenary

Monday AM

Session Chair: D. Hareme

(1.0) 9:00 – 9:15 AM

Welcome

*D. Hareme (IBM, USA)*

(1.1) 9:15 – 10:05 AM

(Invited) Past, Present and Future: SiGe and CMOS Transistor Scaling 3

*K. J. Kuhn, A. Murthy, R. Kotlyar, and M. Kuhn (Intel Corporation, USA)*

(1.2) 10:05 – 10:55 AM

(Invited) Scaling Energy and Form Factor with Germanium Microphotonics n/a

*L. C. Kimerling (Massachusetts Institute of Technology, USA)*

10:55 AM – 12:10 PM

Lunch Break

### Chapter 2 Joint FET – STRAIN

Monday PM

Session Co-Chairs: S. Bedell and Y. C. Yeo

(2.1) 12:10 – 12:40 PM

(Invited) Strain Scaling and Modeling for FETs 21

*V. Moroz and M. Choi (Synopsys, USA)*

(2.2) 12:40 – 1:10 PM	
(Invited) Feasibility of Ge CMOS for Beyond Si-CMOS	33
<i>A. Toriumi, C. Lee, T. Nishimura, K. Kita, S. Wang, M. Yoshida, and K. Nagashio (The University of Tokyo, JST-CREST, Japan)</i>	
(2.3) 1:10 – 1:40 PM	
(Invited) Strain Mapping of Layers and Devices Using Electron Holography	47
<i>A. Claverie (CEMES/CNRS and Université de Toulouse, France), N. Cherkashin, F. Hüe, S. Reboh, F. Houdellier, E. Snoeck, and M. Hÿtch (CEMES/CNRS, France)</i>	
(2.4) 1:40 – 2:10 PM	
(Invited) Applications of Epitaxy for Semiconductor Technology	59
<i>D. K. Sadana, S. Bedell, T. N. Adam, A. Reznicek, and H. He (IBM, USA)</i>	
2:10 – 2:25 PM	
Coffee Break	

### **Chapter 3 Beyond CMOS**

Monday PM	
Session Co-Chairs: T. Krishnamohan and E. Tutuc	
(3.1) 2:25 – 2:55 PM	
(Invited) Beyond CMOS Devices as Enablers of Future Energy Efficient Integrated Circuits and Systems	73
<i>A. M. Ionescu, G. A. Salvatore, and L. Lattanzio (Ecole Polytechnique Fédérale de Lausanne, Switzerland)</i>	
(3.2) 2:55 – 3:25 PM	
(Invited) Strain Engineering and Junction Design for Tunnel Field-Effect Transistor	77
<i>Y. Yeo, G. Han, Y. Yang, and P. Guo (National University of Singapore, Singapore)</i>	
(3.3) 3:25 – 3:55 PM	
(Invited) Growth of MnGe Nanostructures for Spintronics Applications	89
<i>K. Wang and F. Xiu (University of California at Los Angeles, USA)</i>	
(3.4) 3:55 – 4:25 PM	
(Invited) Novel Electronic and Optoelectronic Devices in Germanium Integrated on Silicon	101
<i>K. C. Saraswat (Stanford University, USA)</i>	

4:25 – 7:00 PM  
ECS Plenary and Dinner Break

## Chapter 4 Posters – Short Presentations

Monday PM  
Session Chair: D. Haramé

NOTE:

The following presentations will also be presented as poster displays on  
Tuesday, October 12, 2010 in the Evening Poster Session

- (4.01) 7:00 – 7:05 PM  
Achievement of Excellent C-V Characteristics in GeO<sub>2</sub>/Ge System Using Post Metal  
Deposition Annealing 111  
*H. Koumo, Y. Suzuki, Y. Oniki, Y. Iwazaki, and T. Ueno*  
*(Tokyo University of Agriculture and Technology, Japan)*
- (4.02) 7:05 – 7:10 PM  
Comparative Experimental Study between Diamond and Conventional MOSFET 121  
*S. P. Gimenez and D. M. Alati (Centro Universitário da FEI, Brazil)*
- (4.03) 7:10 – 7:15 PM  
Growth of Epitaxial Silicon-on-Insulator Substrates by Solid State Epitaxy 133  
*E. F. Arkun, G. Vosters, S. Semans, A. Clark, and R. S. Smith (Translucent Inc., USA)*
- (4.04) 7:15 – 7:20 PM  
Laser-Induced Epitaxial Growth (LEG) Technology for Multi-Stacked MOSFETs 143  
*Y. Son (Seoul University, Republic of Korea), K. Hwang, C. Kang (Samsung Electronics, Republic of Korea), and E. Yoon (Seoul University, Republic of Korea)*
- (4.05) 7:20 – 7:25 PM  
Low-Temperature Epitaxial Si, SiGe, and SiC in a 300mm UHV/CVD Reactor 149  
*T. N. Adam, S. Bedell, A. Reznicek, D. K. Sadana, R. J. Murphy (IBM, USA), A. Venkateshan, T. Tsunoda (Canon Anelva Corporation, USA), T. Seino, J. Nakatsuru (Canon Anelva Corporation, Japan), and S. Shinde (Canon Anelva Corporation, USA)*
- (4.06) 7:25 – 7:30 PM  
Monochlorosilane for Low Temperature Silicon Epitaxy 155  
*P. Tomasini (GaNotec Inc., USA) and K. D. Weeks (ASM America, USA)*

(4.07) 7:30 – 7:35 PM		
The Compositional Distribution of the Ge Islands Grown by Ultra-High Vacuum Chemical Vapor Deposition		n/a
<i>H. Chang (National Central University, Taiwan), C. Lee (National Taiwan University, Taiwan), and S. Lee (National Central University, Taiwan)</i>		
(4.08) 7:35 – 7:40 PM		
Formation of Pseudo-Expitaxial Ge Films on Si(100) by Droplet of Ge Microliquid		165
<i>T. Matsumoto, S. Higashi, K. Makihara, M. Akazawa, and S. Miyazaki (Hiroshima University, Japan)</i>		
(4.09) 7:40 – 7:45 PM		
Investigation of Process Parameters on the Properties of Selective Epitaxial Growth SiGe Structure		171
<i>S. Kim, J. Yoo, S. Koo, and D. Ko (Yonsei University, Republic of Korea)</i>		
(4.10) 7:45 – 7:50 PM		
Design, Manufacture and Performance of Germanium Bipolar Transistors		181
<i>K. Li, H. S. Gamble, M. Armstrong, D. W. McNeill, and A. Armstrong (Queen's University of Belfast, UK)</i>		
(4.11) 7:50 – 7:55 PM		
Modeling of NPN-SiGe-HBT Electrical Performance Improvement through Employing Si <sub>3</sub> N <sub>4</sub> Strain in the Collector Region		191
<i>M. S. Al-Sa'di, S. Fregonese, C. Maneux, and T. Zimmer (Université de Bordeaux, France)</i>		
(4.12) 7:55 – 8:00 PM		
> 10 <sup>20</sup> cm <sup>-3</sup> n-Doping in Ge by Sb/P Co-Implants: n <sup>+</sup> /p Diodes with Improved Rectification		201
<i>J. Kim, S. Bedell, and D. K. Sadana (IBM, USA)</i>		
(4.13) 8:00 – 8:05 PM		
Control of Strain Relaxation Behavior of Ge <sub>1-x</sub> Sn <sub>x</sub> Layers for Tensile Strained Ge Layers		205
<i>Y. Shimura (Nagoya University and Japan Society for the Promotion of Science, Japan), S. Takeuchi (Covalent Materials Co., Ltd., Japan), O. Nakatsuka, and S. Zaima (Nagoya University, Japan)</i>		
(4.14) 8:05 – 8:10 PM		
Optical Property of Si <sub>0.8</sub> Ge <sub>0.2</sub> /Si Multilayer Grown by Using RPCVD		211
<i>T. Kim, Y. Kil, M. Shin, T. Jeong, S. Kang, C. Choi, and K. Shim (Chonbuk National University, Republic of Korea)</i>		

(4.15) 8:10 – 8:15 PM	
Improving the Performance of SiGe-Based IR Detectors	221
<i>M. Kolahdouz, A. Afshar Farniya, M. Östling, and H. H. Radamson</i> (Royal Institute of Technology KTH, Sweden)	
(4.16) 8:15 – 8:20 PM	
Formation of Al <sub>2</sub> O <sub>3</sub> Film on Si Substrate by Microwave Generated Remote Plasma Assisted Atomic Layer Deposition Technique	227
<i>H. Ishizaki, M. Iida, Y. Otani, Y. Fukuda (Tokyo University of Science, Suwa, Japan),</i> <i>T. Sato, T. Takamatsu (University of Yamanashi, Japan), and T. Ono</i> (Hirosaki University, Japan)	
(4.17) 8:20 – 8:25 PM	
Characterization of Interface States of HfO <sub>2</sub> /Ge with Fluorine Treatment by Using DLTS/ICTS	235
<i>T. Kanashima, Y. Yoshioka, D. Lee, and M. Okuyama (Osaka University, Japan)</i>	
(4.18) 8:25 – 8:30 PM	
Effect of Heteroatom for Nitrogen Vacancy in SiN/SiO <sub>2</sub> Interface Layer in MONOS-Type Memories	243
<i>K. Yamaguchi, A. Otake, and K. Shiraishi (University of Tsukuba, Japan)</i>	
(4.19) 8:30 – 8:35 PM	
Characterization of Interfaces between Chemically Cleaned or Thermally Oxidized Germanium and Metals	253
<i>H. Murakami, T. Fujioka, A. Ohta, T. Bando, S. Higashi (Hiroshima University, Japan),</i> <i>and S. Miyazaki (Nagoya University, Japan)</i>	
(4.20) 8:35 – 8:40 PM	
Stable Position of B <sub>12</sub> Cluster Near Si(001) Surface and Its STM Images	263
<i>T. Maruizumi and S. Ito (Tokyo City University, Japan)</i>	
(4.21) 8:40 – 8:45 PM	
Electrical and Microstructural Properties of Pt-Germanides Formed on p-type Ge Substrate	n/a
<i>M. Jeong, K. Moon, J. Kim, K. Shin, and C. Choi (Chonbuk National University,</i> <i>Republic of Korea)</i>	
(4.22) 8:45 – 8:50 PM	
Electrical Properties of Yttrium-Titanium Oxide High-k Gate Dielectric on Ge	275
<i>M. K. Bera, P. Ahmet, K. Kakushima, K. Tsutsui, N. Sugii, A. Nishiyama, T. Hattori, and</i> <i>H. Iwai (Tokyo Institute of Technology, Japan)</i>	

## Chapter 5 SiGe BiPolar and BiCMOS Technology

Tuesday AM

Session Co-Chairs: M. Östling and G. Niu

(5.1) 8:00 – 8:20 AM

Enablement and Optimization of SiGe HBTs for Extreme Environment Electronics 287  
*G. Niu (Auburn University, USA)*

(5.2) 8:20 – 8:40 AM

A Physics-Based Trap-Assisted Tunneling Current Model for Cryogenic Temperature Compact Modeling of SiGe HBTs 301  
*Z. Xu, G. Niu, L. Luo (Auburn University, USA), and J. Cressler (Georgia Institute of Technology, USA)*

(5.3) 8:40 – 9:00 AM

$f_{\text{MAX}}$  Increase to 500 GHz of SiGe HBTs at Low Temperature 311  
*N. Zerounian, M. Diallo, F. Aniel (IEF, Université Paris-Sud, France), P. Chevalier, and A. Chantre (STMicroelectronics, France)*

(5.4) 9:00 – 9:20 AM

Technology Computer-Aided Design (TCAD) Feasibility Study of Scaling SiGe HBTs 319  
*R. Camillo-Castillo, A. Stricker, J. Johnson (IBM, USA), A. Appaswamy (Georgia Institute of Technology, USA), R. Malladi, A. Joseph, and D. Harame (IBM, USA)*

(5.5) 9:20 – 9:40 AM

Influence of the Selectively Implanted Collector Integration on +400 GHz  $f_{\text{MAX}}$  Si/SiGe:C HBTs 331  
*T. Lacave (STMicroelectronics and IEMN, France), P. Chevalier, Y. Campidelli, L. Depoyan, L. Berthier, F. André, M. Buczko, G. Avenier (STMicroelectronics, France), C. Gaquière (IEMN, France), and A. Chantre (STMicroelectronics, France)*

(5.6) 9:40 – 10:00 AM

Direct Parameter Extraction Of Base and Emitter Resistances For SiGe HBTs Using DC Data Only 337  
*M. Mudholkar, A. Mantooth (University of Arkansas, USA), G. Niu (Auburn University, USA), and J. Cressler (Georgia Institute of Technology, USA)*

(5.7) 10:00 – 10:20 AM

2.4/5.7-GHz Dual-Band Dual-Conversion Low-IF Downconverter Using 0.35  $\mu\text{m}$  SiGe HBT Technology 349  
*J. Syu, C. Meng, S. Yu (National Chiao Tung University, Taiwan), and G. Huang (National Chiao Tung University and National Nano Device Laboratories, Taiwan)*



10:20 – 10:35 AM  
Coffee Break

## Chapter 6 Tunneling Field Effect Transistors

Tuesday AM  
Session Chair: T. Krishnamohan

- (6.1) 10:35 – 11:05 AM  
(Invited) Are Si/SiGe Tunneling Field-Effect Transistors a Good Idea? 357  
*S. Koester (University of Minnesota – Twin Cities, USA), I. Lauer, A. Majumdar, J. Cai, J. Sleight, S. Bedell, P. Solomon, S. Laux, L. Chang, S. Koswatta, W. Haensch (IBM, USA), P. Tomasini (GaNtec, Inc. USA), and S. Thomas (ASM America, USA)*
- (6.2) 11:05 – 11:35 AM  
(Invited) Boosting the On-Current of Si-Based Tunnel Field-Effect Transistors 363  
*A. S. Verhulst (imec, Belgium), W. G. Vandenberghe, D. Leonelli (imec and Katholieke Universiteit Leuven, Belgium), R. Rooyackers, A. Vandooren, G. Pourtois (imec, Belgium), S. De Gendt, M. M. Heyns, and G. Groeseneken (imec and Katholieke Universiteit Leuven, Belgium)*
- (6.3) 11:35 – 11:55 AM  
Axial Ge/Si Nanowire Heterostructure Tunnel FETs 373  
*S. A. Dayeh and S. Picraux (Los Alamos National Laboratory, USA)*
- (6.4) 11:55 AM – 12:15 PM  
Room-Temperature Resonant Tunneling Diode with High-Ge-Fraction Strained Si<sub>1-x</sub>Ge<sub>x</sub> and Nanometer-Order Ultrathin Si 379  
*M. Sakuraba, K. Takahashi, and J. Murota (Tohoku University, Japan)*

12:15 – 1:30 PM  
Lunch Break

## Chapter 7

### Processing I: SiGe, Ge, and C Processing

Tuesday PM

Session Co-Chairs: J. Fiorenza and Y.-H. Xie

(7.1) 1:30 – 2:00 PM

(Invited) Cyclic Deposition/Etch Processes for the Formation of Si Raised Sources and Drains in Advanced MOSFETs 391

*J. Hartmann, M. Py, P. Morel, T. Ernst, B. Prévitali, J. Barnes (CEA-LETI, France), N. Vulliet (STMicroelectronics, France), N. Cherkashin, S. Reboh, M. Hÿtch, and V. Paillard (CEMES/CNRS, France)*

(7.2) 2:00 – 2:30 PM

(Invited) High-Mobility Ge on Insulator (GOI) by SiGe Mixing-Triggered Rapid-Melting-Growth 409

*T. Sadoh and M. Miyao (Kyushu University, Japan)*

(7.3) 2:30 – 2:50 PM

Formation of High Aspect-Ratio Ge-Fin Structures with {110} Facets by Anisotropic Wet Etching 419

*Y. Moriyama, K. Ikeda, Y. Kamimuta, and T. Tezuka (MIRAI-Toshiba, Japan)*

(7.4) 2:50 – 3:10 PM

Germanium on Nothing for Nanowire Devices 425

*P. M. Thomas, D. Pawlik, E. Freeman, B. Romanczyk, and S. Rommel (Rochester Institute of Technology, USA)*

3:10 – 3:25 PM

Coffee Break

## Chapter 8

### Surfaces and Interfaces I: Surface Passivation and High-K Dielectric Interfaces

Tuesday PM

Session Co-Chairs: S. Miyazaki and S. Zaima

(8.1) 3:25 – 3:55 PM

(Invited) Ge Surfaces and Its Passivation by Rare Earth Lanthanum Germanate Dielectric 433

*A. Dimoulas, D. Tsoutsou, S. Galata, Y. Panayiotatos, G. Mavrou, and E. Golias (NCSR DEMOKRITOS, Greece)*

(8.2) 3:55 – 4:15 PM	
Monolayer Passivation of Ge(100) Surface via Nitridation and Oxidation	447
<i>J. S. Lee, S. R. Bishop, T. Kaufman-Osborn, E. Chagarov, and A. C. Kummel</i> (University of California at San Diego, USA)	
(8.3) 4:15 – 4:45 PM	
(Invited) <i>In Situ</i> XPS in Atomic Layer Deposition of Oxides on Ge (100)	455
<i>S. Swaminathan and P. C. McIntyre (Stanford University, USA)</i>	
(8.4) 4:45 – 5:15 PM	
(Invited) Formation of Dipole Layers at Oxide Interfaces in High-k Gate Stacks	463
<i>K. Kita (The University of Tokyo, JST-CREST, Japan), L. Zhu (The University of Tokyo, Japan), T. Nishimura, K. Nagashio, and A. Toriumi</i> (The University of Tokyo, JST-CREST, Japan)	
(8.5) 5:15 – 5:35 PM	
Control of Gate Metal Effective Work Functions and Interface Layer Thickness by Designing Interface Thermodynamics Based on Heteroatom Incorporation into High-k HfO <sub>2</sub> Gate Dielectrics	479
<i>K. Shiraishi (University of Tsukuba, Japan), T. Hosoi, H. Watanabe (Osaka University, Japan), and K. Yamada (University of Tsukuba, Japan)</i>	
5:35 PM	
Dinner	
6:00 – 8:30 PM	
Tuesday Evening Poster Session – Short Presentations from Chapter 4	

## Chapter 9

### Strain Engineering in Advanced Device Structures

Wednesday AM	
Session Co-Chairs: S. Bedell and Y. C. Yeo	
(9.1) 8:00 – 8:30 AM	
(Invited) Strain Engineering for Fully-Depleted SOI Devices	489
<i>A. Khakifirooz, P. Kulkarni, S. Bedell, K. Cheng, D. K. Sadana, B. Doris, and G. Shahidi</i> (IBM, USA)	
(9.2) 8:30 – 8:50 AM	
Critical Factors for Enhancement of Compressive Strain in SGOI Layers Fabricated by Ge Condensation Technique	501
<i>S. Takagi, K. Tomiyama, S. Dissanayake, and M. Takenaka (The University of Tokyo, Japan)</i>	

(9.3) 8:50 – 9:10 AM	
Strain Stability in Nanoscale Patterned Strained Silicon-On-Insulator	511
<i>O. Moutanabbir (Max Planck Institute of Microstructure Physics, Germany and RIKEN Advanced Science Institute, Japan), M. Reiche, A. Hähnel, W. Erfurth (Max Planck Institute of Microstructure Physics, Germany), A. Tarun, N. Hayazawa, S. Kawata (RIKEN Advanced Science Institute, Japan), F. Naumann, and M. Petzold (Fraunhofer Institute for Mechanics of Materials, Germany)</i>	
(9.4) 9:10 – 9:30 AM	
Effects of Growth and Surface Cleaning Conditions on Strain Relaxation on SiGe Films beyond a Critical Thickness on Si(001) Substrate	523
<i>J. Park, M. Ishii, R. Balasubramanian, Y. Kim, and S. Kuppurao (Applied Materials Inc., USA)</i>	
(9.5) 9:30 – 10:00 AM	
(Invited) Assessment of Ge <sub>1-x</sub> Sn <sub>x</sub> Alloys for Strained Ge CMOS Devices	529
<i>S. Takeuchi (Nagoya University, Japan and Covalent Silicon Co., Japan), Y. Shimura, T. Nishimura (Nagoya University, Japan), B. Vincent, G. Eneman, T. Clarysse (imec, Belgium), J. Demeulemeester, K. Temst, A. Vantomme (Katholieke Universiteit Leuven, Belgium), J. Dekoster, M. Caymax, R. Loo (imec, Belgium), O. Nakatsuka (Nagoya University, Japan), A. Sakai (Osaka University, Japan), and S. Zaima (Nagoya University, Japan)</i>	
10:00 – 10:15 AM	
Coffee Break	

## Chapter 10 Optoelectronics I: Light Emission and Novel Heterostructures

Wednesday AM	
Session Co-Chairs: G. Masini and Y. Kang	
(10.1) 10:15 – 10:45 AM	
(Invited) Band-Engineered Ge-On-Si Lasers for Integrated Photonics	539
<i>J. Liu (Massachusetts Institute of Technology and Dartmouth College, USA), X. Sun, R. Camacho-Aguilera, L. C. Kimerling, and J. Michel (Massachusetts Institute of Technology, USA)</i>	
(10.2) 10:45 – 11:15 AM	
(Invited) Characterizations of Direct Band Gap Photoluminescence and Electroluminescence from epi-Ge on Si	545
<i>S. Cheng, G. Shambat, J. Lu, H. Yu, K. C. Saraswat, J. Vuckovic, and Y. Nishi (Stanford University, USA)</i>	

(10.3) 11:15 – 11:35 AM	
Extrinsic Effects of Indirect Radiative Transition of Ge	555
<i>S. Jan, C. Lee, T. Cheng, Y. Chen, K. Peng, S. Chan (National Taiwan University, Taiwan), C. Liu (National Taiwan University and National Nano Device Laboratories, Taiwan), Y. Yamamoto (IHP, Germany), and B. Tillack (IHP and TU Berlin, Germany)</i>	
(10.4) 11:35 – 11:55 AM	
Enhancements of Direct Band Radiative Recombination from Ge	563
<i>T. Cheng, K. Peng, C. Ko, C. Chen, S. Chan (National Taiwan University, Taiwan), and C. Liu (National Taiwan University and National Nano Device Laboratories, Taiwan)</i>	
(10.5) 11:55 AM – 12:25 PM	
(Invited) Optoelectronic Monolithic Integration of Waveguided Metal-Germanium-Metal Photodetector and Ge CMOSFETs on SOI Wafer	573
<i>H. Zang, J. Wang (National University of Singapore and Institute of Microelectronics, Singapore), G. Lo (Institute of Microelectronics, Singapore), and S. Lee (National University of Singapore, Singapore)</i>	
12:25 – 1:25 PM	
Lunch Break	

## Chapter 11 Epitaxy I: Si and SiGe Epitaxial Processes and Devices

Wednesday PM (Concurrent Session)

Session Chair: M. Caymax

(11.1) 1:25 – 1:45 PM	
Kinetic Model of SiGe Selective Epitaxial Growth Using RPCVD Technique	581
<i>M. Kolahdouz, L. Maresca, R. Ghandi, A. Khatibi, and H. H. Radamson (Royal Institute of Technology KTH, Sweden)</i>	
(11.2) 1:45 – 2:05 PM	
300mm Cold-Wall UHV/CVD Reactor for Low-Temperature Epitaxial (100) Silicon	595
<i>T. N. Adam, S. Bedell, A. Reznicek, D. K. Sadana (IBM, USA), A. Venkateshan, T. Tsunoda (Canon Anelva Corporation, USA), T. Seino, J. Nakatsuru (Canon Anelva Corporation, Japan), and S. Shinde (Canon Anelva Corporation, USA)</i>	
(11.3) 2:05 – 2:35 PM	
(Invited) Atomic Control of Doping during Si Based Epitaxial Layer Growth Processes	603
<i>B. Tillack (IHP and TU Berlin, Germany), Y. Yamamoto (IHP, Germany), and J. Murota (Tohoku University, Japan)</i>	

(11.4) 2:35 – 3:05 PM  
(Invited) Si-Ge-Sn Technologies: From Molecules to Materials to Prototype Devices 615  
*J. Kouvetakis, J. Tolle, J. Mathews, R. Roucka, and J. Menéndez*  
(Arizona State University, USA)

(11.5) 3:05 – 3:35 PM  
(Invited) Selective Epitaxial Growth (SEG) of Highly Doped Si:P on Source/Drain Areas of 629  
NMOS Devices Using Si<sub>3</sub>H<sub>8</sub>/PH<sub>3</sub>/Cl<sub>2</sub> Chemistry  
*M. Bauer and S. Thomas (ASM America, USA)*

3:35 – 3:50 PM  
Coffee Break

## Chapter 12 Quantum Dots

Wednesday PM (Concurrent Session)  
Session Chair: M. Eriksson

(12.1) 1:25 – 1:55 PM  
(Invited) Toward Si/SiGe Quantum Dot Spin Qubits: Gated Si/SiGe Single and Double 639  
Quantum Dots  
*C. Simmons, J. Prance, M. Thalakulam, B. Rosemeyer, B. Van Bael, D. E. Savage,*  
*M. G. Lagally, R. Joynt, M. Friesen, S. Coppersmith, and M. Eriksson*  
(University of Wisconsin-Madison, USA)

(12.2) 1:55 – 2:15 PM  
Surface Orientation Effects on SiGe Quantum Dots and Nanorings Formation 649  
*C. Lee, W. Tu, C. Lin (National Taiwan University, Taiwan), H. Chang, S. Lee*  
(National Central University, Taiwan), and *C. Liu (National Taiwan University and*  
*National Nano Device Laboratories, Taiwan)*

(12.3) 2:15 – 2:35 PM  
Self-Align Formation of Si Quantum Dots 661  
*K. Makihara, M. Ikeda (Hiroshima University, Japan), H. Deki*  
(Hiroshima Kokusai Gakuin University, Japan), *A. Ohta (Hiroshima University, Japan),*  
and *S. Miyazaki (Nagoya University, Japan)*

2:35 PM  
Break of Concurrent Session

## Chapter 13 Nanowire Growth, Processing, and Devices

Wednesday PM

Session Co-Chairs: N. D. Nguyen and B. Tillack

(13.1) 3:50 – 4:20 PM

(Invited) Fabrication and Properties of Abrupt Si-Ge Heterojunction Nanowire Structures 671  
*C. Wen (Purdue University, USA), M. Reuter, J. Tersoff (IBM, USA), E. Stach (Purdue University, USA), and F. Ross (IBM, USA)*

(13.2) 4:20 – 4:40 PM

Ge/Si Core/Multishell Heterostructure FETs 681  
*S. A. Dayeh and S. Picraux (Los Alamos National Laboratory, USA)*

(13.3) 4:40 – 5:00 PM

Advanced Strained-Silicon and Core-Shell Si/Si<sub>1-x</sub>Ge<sub>x</sub> Nanowires for CMOS Transport Enhancement 687  
*P. Hashemi (MIT Microsystems Technology Laboratories, USA), C. Poweleit, M. Canonico (Arizona State University, USA), and J. Hoyt (MIT Microsystems Technology Laboratories, USA)*

(13.4) 5:00 – 5:20 PM

Vapor-Liquid-Solid Growth of Si<sub>1-x</sub>Ge<sub>x</sub> and Ge/Si<sub>1-x</sub>Ge<sub>x</sub> Axial Heterostructured Nanowires 699  
*S. Minassian, X. Weng, and J. Redwing (Pennsylvania State University, USA)*

(13.5) 5:20 – 5:40 PM

Ge/Si Core/Shell Nanowire Structures for Tunneling Devices 707  
*J. T. Smith, Y. Zhao, A. Razavieh, C. Yang, and J. Appenzeller (Purdue University, USA)*

5:40 – 7:25 PM

Dinner Break

**Chapter 14**  
**Workshop: Group-IV Lasers - Can They Ever Compete With III-Vs?**

Wednesday PM

Session Chair: S. Koester

7:25 – 7:55 PM

Workshop Mixer

(14.1) 7:55 – 8:10 PM

(Invited) Practical Strategies for Tuning Optical, Structural and Thermal Properties in Group IV Ternary Semiconductors

717

*A. V. Chizmeshya and J. Kouvetakis (Arizona State University, USA)*

(14.2) 8:10 – 8:25 PM

(Invited) High Power Waveguide Ge/Si Photodiodes

729

*J. E. Bowers, M. Piels, A. Ramaswamy, and T. Yin  
(University of California at Santa Barbara, USA)*

(14.3) 8:25 – 8:40 PM

(Invited) Band-Engineered Ge-on-Si Lasers for Integrated Photonics

n/a

*J. Liu (Massachusetts Institute of Technology and Dartmouth College, USA)*

8:40 – 9:40 PM

Workshop Panel Discussion

**Chapter 15**  
**Optoelectronics II: Photodetectors**

Thursday AM

Session Co-Chairs: J. Liu and G. Masini

(15.1) 8:00 – 8:30 AM

(Invited) Performance and Reliability of a 25Gb/s Ge Waveguide Photodetector Integrated in a CMOS Process

741

*S. Sahni, D. Song, M. Sharp, D. Kucharski, D. Guckenberger, and G. Masini  
(Luxtera Inc., USA)*

(15.2) 8:30 – 9:00 AM

(Invited) Integration of Germanium Avalanche Photodetectors on Silicon for On-Chip Optical Interconnects

749

*S. Assefa, F. Xia, and Y. Vlasov (IBM, USA)*



(15.3) 9:00 – 9:30 AM  
(Invited) Ge/Si Waveguide Avalanche Photodiodes on SOI Substrates for High Speed Communication 757  
*Y. Kang (Intel Corporation, USA), Y. Saado (Numonyx Corporation, Israel), M. Morse, M. Paniccia (Intel Corporation, USA), J. Campbell (University of Virginia, USA), J. E. Bowers (University of California at Santa Barbara, USA), and A. Pauchard (self)*

(15.4) 9:30 – 9:50 AM  
Near IR Photodiodes with Tunable Absorption Edge Based on Ge<sub>1-y</sub>Sn<sub>y</sub> Alloys Integrated on Silicon 765  
*J. Mathews, R. Roucka, C. Weng, R. Beeler, J. Tolle, J. Menéndez, and J. Kouvetakis (Arizona State University)*

9:50 – 10:05 AM  
Coffee Break

## Chapter 16 Nano Membranes and MEMs

Thursday AM (Concurrent Session)  
Session Chair: T. Krishnamohan

(16.1) 10:05 – 10:35 AM  
(Invited) Si, SiGe, Ge, and III-V Semiconductor Nanomembranes and Nanowires Enabled by SiGe Epitaxy 777  
*M. Orlowski, C. Ndoye, T. Liu, and M. Hudait (Virginia Tech, USA)*

(16.2) 10:35 – 10:55 AM  
Diffusion and Interface Segregation of Phosphorus and Boron in Bulk Germanium, Germanium Nanomembranes, and Nanowires 791  
*T. Liu, C. Ndoye, and M. Orlowski (Virginia Tech, USA)*

(16.3) 10:55 – 11:25 AM  
(Invited) SiGe MEMS Technology: A Platform Technology Enabling Different Demonstrators 799  
*A. Witvrouw, R. Van Hoof, G. Bryce, B. Du Bois, A. Verbist, S. Severi, L. Haspeslagh, H. Osman, J. De Coster (imec, Belgium), L. Wen (Katholieke Universiteit Leuven, Belgium), R. Puers (imec and Katholieke Universiteit Leuven, Belgium), R. Beernaert, H. De Smet, S. Rudra, and D. Van Thourhout (Universiteit Ghent, Belgium)*

(16.4) 11:25 – 11:45 AM  
Elastically Strain-Sharing Si(110) Nanomembranes 813  
*D. M. Paskiewicz, S. A. Scott, D. E. Savage, and M. G. Lagally (University of Wisconsin-Madison, USA)*

(16.5) 11:45 AM – 12:05 PM  
Functionalized Back-End Devices for (Bi)CMOS Circuits 823  
*C. Wenger, C. Walczyk, D. Walczyk, M. Lukosius, M. Fraschke, D. Wolansky (IHP, Germany), and P. Santos (PDI, Germany)*

12:05 – 1:20 PM  
Lunch Break

## Chapter 17 Epitaxy II: Epitaxy of Alternative Semiconductors on Si Substrates

Thursday AM (Concurrent Session)  
Session Chair: M. Caymax

(17.1) 10:05 – 10:35 AM  
(Invited) Epitaxial Growth of III-Nitrides on Silicon Substrates 833  
*S. Degroote (EpiGaN bvba, Belgium), M. Leys, K. Cheng, B. Sijmus, J. Derluyn, G. Borghs, and M. Germain (imec, Belgium)*

(17.2) 10:35 – 10:55 AM  
High Quality Epitaxial Growth of GaAs<sub>y</sub>P<sub>1-y</sub> Alloys on Si<sub>1-x</sub>Ge<sub>x</sub> Virtual Substrates 843  
*P. Sharma, M. T. Bulsara, and E. A. Fitzgerald (Massachusetts Institute of Technology, USA)*

(17.3) 10:55 – 11:25 AM  
(Invited) Direct Heterointegration of III-V Materials on Group IV Substrates 849  
*D. A. Ahmari, B. McDermott (EpiWorks, Inc., USA), S. Thomas (ASM America, USA), B. Roof, Q. Hartmann (EpiWorks, Inc., USA), and X. Li (University of Illinois, USA)*

(17.4) 11:25 – 11:55 AM  
(Invited) Epitaxial Formation of Graphene on Si Substrates: From Heteroepitaxy of 3C-SiC to Si Sublimation 859  
*M. Suemitsu (CREST/Japan Science and Technology Agency and Tohoku University, Japan), H. Handa, E. Saito, and H. Fukidome (Tohoku University, Japan)*

11:55 AM – 1:20 PM  
Lunch Break

## Chapter 18

### Surfaces and Interfaces II: Interface Physics, Characterization, and Device Application

Thursday PM (Concurrent Session)

Session Co-Chairs: S. Miyazaki and S. Zaima

(18.1) 1:20 – 1:40 PM

Novel SiGe Source/Drain for Reduced Parasitic Resistance in Ge NMOS 871

*S. Raghunathan (Stanford University, USA), T. Krishnamohan (Intel, USA), and  
K. C. Saraswat (Stanford University, USA)*

(18.2) 1:40 – 2:00 PM

Non-Contact and Non-Destructive Measurement of Ge and B Content in Si<sub>1-x</sub>Ge<sub>x</sub>/Si Using  
Very High Resolution Multiwavelength Raman Spectroscopy 877

*W. Yoo, T. Ueda, T. Ishigaki, and K. Kang (WaferMasters, Inc., USA)*

(18.3) 2:00 – 2:20 PM

X-ray Microdiffraction Study on Crystallinity of Micron-Sized Ge Films Selectively Grown  
on Si(001) Substrates 887

*K. Ebihara, S. Harada, J. Kikkawa, Y. Nakamura, A. Sakai (Osaka University, Japan),  
G. Wang, M. Caymax (imec, Belgium), Y. Imai, S. Kimura, and O. Sakata  
(JASRI/SPring-8, Japan)*

(18.4) 2:20 – 2:40 PM

Interface Reaction and Rate Enhancement of SiGe Thermal Oxidation 893

*T. Shimura, Y. Okamoto, D. Shimokawa, T. Inoue, T. Hosoi, and H. Watanabe  
(Osaka University, Japan)*

(18.5) 2:40 – 3:00 PM

Misfit Stress Relaxation Mechanism in GeO<sub>2</sub>/Ge Systems: A Classical Molecular Simulation  
Study 901

*T. Watanabe, T. Onda, and I. Ohdomari (Waseda University, Japan)*

(18.6) 3:00 – 3:20 PM

Chemical Trend of Schottky-Barrier Change by Segregation Layers at Metal/Si Interfaces:  
First-Principles Study 913

*T. Nakayama, Y. Maruta, and K. Kobinata (Chiba University, Japan)*

3:20 – 3:35 PM

Coffee Break

## Chapter 19 Related Compounds I

Thursday PM (Concurrent Session)

Session Chair: A. Reznicek

(19.1) 1:20 – 1:50 PM

(Invited) III-V Photovoltaics: Recent Developments and Prospects 923  
*N. Sosa, T. Van Kessel, Y. Martin, and H. Hovel (IBM, USA)*

(19.2) 1:50 – 2:20 PM

(Invited) Ge/III-V Heterostructures and Their Applications in Fabricating Engineered Substrates 927  
*Y. Bai and E. A. Fitzgerald (Massachusetts Institute of Technology, USA)*

(19.3) 2:20 – 2:50 PM

(Invited) Selective Epitaxial Growth of III-V Semiconductor Heterostructures on Si Substrates for Logic Applications 933  
*N. Nguyen (imec and Université de Liège, Belgium), G. Wang, G. Brammertz, M. Leys, N. Waldron, G. Winderickx, K. Lismont, J. Dekoster, R. Loo, M. Meuris (imec, Belgium), S. Degroote (EpiGaN bvba, Belgium), F. Buttitta, B. O'Neil, O. Féron, J. Lindner, F. Schulte, B. Schineller, M. Heuken (AIXTRON AG, Germany), and M. Caymax (imec, Belgium)*

(19.4) 2:50 – 3:10 PM

Growth and Optical Properties of InGaAs via Ge-Based Virtual Substrates: A New Chemistry Based Strategy 941  
*R. Beeler, C. Weng, J. Tolle, R. Roucka, J. Mathews (Arizona State University, USA), D. A. Ahmari (EpiWorks, Inc., USA), J. Menéndez, and J. Kouvetakis (Arizona State University, USA)*

3:10 – 3:35 PM

Coffee Break

## Chapter 20 Processing II: Advances in Structures, Doping, and Annealing

Thursday PM (Concurrent Session)

Session Co-Chairs: J.-M. Hartmann and T. Sadoh

(20.1) 3:35 – 4:05 PM

(Invited) Graphene FETs: Promises and Challenges 953  
*C. Miao, Y. Park, W. Liu, Y. Wang, J. Zhu, B. Huang, J. Woo, and Y. Xie (University of California at Los Angeles, USA)*

(20.2) 4:05 – 4:35 PM	
(Invited) Aspect Ratio Trapping: A Unique Technology for Integrating Ge and III-Vs with Silicon CMOS	963
<i>J. G. Fiorenza, J. Park, J. Hydrick, J. Li (AmberWave Systems Corporation, USA), J. Li, M. Curtin, M. Carroll, and A. Lochtefeld (AmberWave Inc., USA)</i>	
(20.3) 4:35 – 4:55 PM	
Epitaxial Growth on High Aspect Ratio Structures	977
<i>S. Chopra, V. Tran, B. Wood, Y. Kim, and S. Kuppurao (Applied Materials, USA)</i>	
(20.4) 4:55 – 5:15 PM	
HCl Selective Etching of SiGe versus Si in Stacks Grown on (110)	985
<i>J. Hartmann, V. Destefanis (CEA-LETI, Minatec, France), G. Rabillé, and S. Monfray (STMicroelectronics, France)</i>	
(20.5) 5:15 – 5:35 PM	
Phosphorus Atomic Layer Doping in Si Using PH <sub>3</sub>	995
<i>Y. Yamamoto (IHP, Germany), J. Murota (Tohoku University, Japan), and B. Tillack (IHP and TU Berlin, Germany)</i>	
(20.6) 5:35 – 5:55 PM	
Non-Contact and Non-Destructive Characterization of Laser Spike Annealed Si <sub>1-x</sub> Ge <sub>x</sub> /Si Using Very High Resolution Multiwavelength Raman Spectroscopy	1003
<i>W. Yoo, T. Ueda, T. Ishigaki, and K. Kang (WaferMasters, Inc., USA)</i>	

## Chapter 21 Related Compounds II

Thursday PM (Concurrent Session)

Session Chair: A. Reznicek

(21.1) 4:35 – 4:55 PM	
Defect-Induced Surface Morphological Evolution in Epitaxial Germanium Growth on Silicon	1015
<i>Y. Huang, X. Tao, M. Jin, C. Wang, and E. Sanchez (Applied Materials, Inc., USA)</i>	
(21.2) 4:55 – 5:15 PM	
Observation of CMOS Device Channel Strain Using In-line HRXRD	n/a
<i>J. R. Holt, A. Madan, C. Murray (IBM, USA), M. V. Holt (Argonne National Lab, USA), S. Bedell, E. C. Harley, T. N. Adam, T. Pinto, D. Schepis (IBM, USA)</i>	

(21.3) 5:15 – 5:35 PM	
Self-Aligned NiGeSi Contacts on Gallium Arsenide for III-V MOSFETs	1021
<i>X. Zhang, H. Guo, H. Chin, X. Gong, P. Lim, and Y. Yeo</i>	
<i>(National University of Singapore, Singapore)</i>	
Author Index	1029