2012 IEEE Asia Pacific Conference on Circuits and Systems

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APCCAS 2012 Technical Program

A1L-AAdvanced Circuit Techniques for Implantable Human-IC Interface Microsystems

Time: Monday, December 3, 13:30 - 15:10
Location: Room of Coral
Chair: Anh Tuan Do, *Nanyang Technological University, Singapore*

A1L-B High-Precision Analog-to-Digital and Digital-to-Analog Converters

Time: Monday, December 3, 13:30 - 15:10
Location: Room of Crystal
Chair: Akira Yasuda, *Hosei University, Japan*

A1L-C DSP for Communication

Time: Monday, December 3, 13:30 - 15:10
Location: Room of Pearl
Chair: Izzet Kale, *University of Westminster, UK*

A1L-D Power Converter and Applications

Time: Monday, December 3, 13:30 - 15:10
Location: Room of Alexandrite
Chairs: Tadashi *Suetsugu, Fukuoka University, Japan*
Hirotaka Koizumi, *Tokyo University of Science, Japan*

A2L-A Circuits and Systems for Bio-inspired Systems and Prosthetic Devices (BioPro)

Time: Monday, December 3, 15:40 - 17:20
Location: Room of Coral
Chair: Kea-Tiong Tang, *National Tsing Hua University, Taiwan*

A2L-B Nonlinear Circuits and Systems

Time: Monday, December 3, 15:40 - 17:20
Location: Room of Crystal
Chair: Tadashi Tsubone, *Nagaoka University of Technology, Japan*

A2L-C Low Power VLSI Design

Time: Monday, December 3, 15:40 - 17:20
Location: Room of Pearl
Chairs: Ko-Chi Kou, *National Sun Yet-sen University, Taiwan*
Zhi Hui Kong, *Nanyang Technological University, Singapore*

A2L-D Visual Signal Processing & Communications

Time: Monday, December 3, 15:40 - 17:20
Location: Room of Alexandrite
Chair: Po-Ming Lee, *Southern Taiwan University, Taiwan*

A3P-F Analog Circuit Design Techniques

Time: Monday, December 3, 15:40 - 17:20
Location: Room of Amber
Chair: Shigetoshi Nakatake, *The University of Kitakyushu, Japan*

A3P-H Biomedical Circuits and Systems (III)

Time: Monday, December 3, 15:40 - 17:20
Location: Room of Amber
Chair: Hsueh Tao Chou, *National Yunlin University of Science and Technology, Taiwan*

B1L-A Low Power RF/mm-Wave IC Design and Technology (I)

Time: Tuesday, December 4, 10:45 - 12:15
Location: Room of Coral
Chair: Kiat Seng Yeo, *Nanyang Technological University, Singapore*

B1L-B Nonlinear Oscillations

Time: Tuesday, December 4, 10:45 - 12:15
Location: Room of Crystal
Chair: Toshimichi Saito, *Hosei University, Japan*

B1L-C Low Complexity Circuits and Systems for Communications

Time: Tuesday, December 4, 10:45 - 12:15
Location: Room of Pearl
Chair: Yuan-Hao Huang, *National Tsing-Hua University, Taiwan*

B1L-D Biomedical Circuits and Systems (I)

Time: Tuesday, December 4, 10:45 - 12:15
Location: Room of Alexandrite
Chair: Hsueh Tao Chou, *National Yunlin University of Science and Technology, Taiwan*

B2L-A LowPower RF/mm-Wave IC Design and Technology (II)

Time: Tuesday, December 4, 13:30 - 15:10
Location: Room of Coral
Chair: Kaixue Ma, *Nanyang Technological University, Singapore*

B2L-B ADC Design Techniques

Time: Tuesday, December 4, 13:30 - 15:10
Location: Room of Crystal
Chair: Masao Hotta, *Tokyo City University, Japan*

B2L-C Digital Filter Design

Time: Tuesday, December 4, 13:30 - 15:10
Location: Room of Pearl
Chair: Takao Hinamoto, *Hiroshima University, Japan*

B2L-D Biomedical Circuits and Systems (II)

Time: Tuesday, December 4, 13:30 - 15:10
Location: Room of Alexandrite
Chair: Hsueh Tao Chou, *National Yunlin University of Science and Technology, Taiwan*

B3P-F Circuits and Systems for Communications

Time: Tuesday, December 4, 13:30 - 15:10
Location: Room of Amber
Chair: Tzung-Je Lee, *Cheng Shiu University, Taiwan*

B3P-G Power Electronics and Power Integrated Circuits

Time: Tuesday, December 4, 13:30 - 15:10
Location: Room of Amber
Chairs: Tara Julia Hamilton, *University of New South Wales, Australia*
Hiroo Sekiya, *Chiba University, Japan*

B3P-H Artificial Neural Network Systems

Time: Tuesday, December 4, 13:30 - 15:10
Location: Room of Amber
Chair: Hiroyuki Asahara, *Oita University, Japan*

B4L-A Specific Embedded System Design

Time: Tuesday, December 4, 15:40 - 17:20
Location: Room of Coral
Chairs: Ching-Lung Su, *National Yunlin University of Science & Technology, Taiwan*
Shanq-Jang Ruan, *National Taiwan University of Science & Technology, Taiwan*

B4L-B Designs and Techniques for System-on-Chip

Time: Tuesday, December 4, 15:40 - 17:20
Location: Room of Crystal
Chairs: Lih-Yih Chiou, *National Cheng Kung University, Taiwan*
Chh-Wei Liu, *National Chiao Tung University, Taiwan*

B4L-C VLSI Systems

Time: Tuesday, December 4, 15:40 - 17:20
Location: Room of Pearl
Chair: Ya-Hsin Hsueh, *National Yunlin University of Science and Technology, Taiwan*

B4L-D Power Integrated Circuits

Time: Tuesday, December 4, 15:40 - 17:20
Location: Room of Alexandrite
Chairs: Wing-Hung Ki, *The Hong Kong University of Science and Technology, China*
Yasunori Kobori, *Gunma University, Japan*

C1L-A Embedded Graphic Processing Unit

Time: Wednesday, December 5, 10:45 - 12:15
Location: Room of Coral
Chair: Shau-Yin Tseng, *Industrial Technology Research Institute, Taiwan*

C1L-B Nanoelectronics and Gigascale Systems

Time: Wednesday, December 5, 10:45 - 12:15
Location: Room of Crystal
Chairs: Zhi Hui Kong, *Nanyang Technological University, Singapore*
Tzung-Je Lee, *Cheng Shiu University, Taiwan*

C1L-C Circuits and Systems for LDPC Decoder and UWB Receiver

Time: Wednesday, December 5, 10:45 - 12:15
Location: Room of Pearl
Chair: Hongchin Lin, *National Chun Hsing University, Taiwan*

C1L-D Neural Systems and Applications

Time: Wednesday, December 5, 10:45 - 12:15
Location: Room of Alexandrite
Chair: Akio Tsuneda, *Kumamoto University, Japan*

C2L-A VLSI Design and Applications of High-Performance Arithmetic and Reliability Computing Units

Time: Wednesday, December 5, 13:30 - 15:10
Location: Room of Coral
Chair: Tso-Bing Juang, *National Pingtung Institute of Commerce, Taiwan*

C2L-B High-Performance Analog Circuit Designs

Time: Wednesday, December 5, 13:30 - 15:10
Location: Room of Crystal
Chair: Akira Hyogo, *Tokyo University of Science, Japan*

C2L-C Multimedia Systems and Applications (I)

Time: Wednesday, December 5, 13:30 - 15:10
Location: Room of Pearl
Chair: Shau-Yin Tseng, *Information and Communications Research Lab., Taiwan*

C2L-D Power and Energy

Time: Wednesday, December 5, 13:30 - 15:10
Location: Room of Alexandrite
Chairs: Matthias *Lechtenberg, TU Dortmund, Germany*
Li Wang*, National Cheng Kung University, Taiwan*

C3P-F Digital Signal Processing

Time: Wednesday, December 5, 15:40 - 17:20
Location: Room of Amber
Chair: Chien-Cheng Tseng*, National Kaohsiung First University of Science and Technology, Taiwan*

C3P-G VLSI Circuits for Security, Image and Flash Memory

Time: Wednesday, December 5, 15:40 - 17:20
Location: Room of Amber
Chairs: Ching-Che Chung, *National Chung-Cheng University, Taiwan*
Tzung-Je Lee, *Cheng Shiu University, Taiwan*

C4L-A Advanced Design Automation Techniques

Time: Wednesday, December 5, 15:40 - 17:20
Location: Room of Coral
Chairs: Shu-Min Li, *National Sun Yat-sen University, Taiwan*
Tai-Chen Chen, *National Central University, Taiwan*

C4L-B Coupled Nonlinear Circuits

Time: Wednesday, December 5, 15:40 - 17:20
Location: Room of Crystal
Chair: Takuji Kousaka, *Oita University, Japan*

C4L-C Multimedia Systems and Applications (II)

Time: Wednesday, December 5, 15:40 - 17:20
Location: Room of Pearl
Chair: Shau-Yin Tseng, *Industrial Technology Research Institute, Taiwan*

C4L-D Test and Yield-Enhancement Techniques

Time: Wednesday, December 5, 15:40 - 17:20
Location: Room of Alexandrite
Chair: Tong-Yu Hsieh, *National Sun Yat-Sen University, Taiwan*